



## STAFF PROFILE



**Ms. Manisha Mohan Nilkanth**

Assistant Professor

Electronics & TeleCommunication

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*Date of Joining*  
22/02/2010

*Nature of Appointment*  
Contractual

### Educational Details

Qualification	Degree	University	Grade	Year
HSC	SCIENCE	KOLHAPUR	70.33%	2001
Graduation	BE -ELECTRONICS	Shivaji Uni., Kolhapur	71.20%	2005
PG	ME -ELECTRONICS - DIGITAL SYSTEMS	PUNE	7.2 (CGPA)	2013

### Experience Details

Type	Duration	Designation/ Organisation Name	Brief Job Profile
Industrial	9 month july2005- march 2006	Electronics Engg Udhyamshri Engineers, Miraj, Sangli	Calibrator
Educational	6 months Aug 2009- Feb 2010	Lecturer P G Moze college of Engg, Wagholi, Pune	Sub: Embedded System
Educational	7Y 6M 22nd Feb 2010 Till Date	Asst Prof Army Institute of Technology, Pune	Sub: Microcontroller and Applications, Digital electronics, Integrated Circuits, Basic electronics Engg., Artificial Intelligence, Mini Projects

## Journal Publications

1. Manisha Nilkanth, (2019). [Smart and Optimized Dough Maker for Domestic Use](https://ipasj.org/IJEC/Volume7Issue5/IJEC-2019-05-16-2.pdf). *IPASJ International Journal of Electronics & Communication (IJEC)*. 7(5), 6-10. ISBN/ ISSN No : ISSN 2321-5984. URL : <https://ipasj.org/IJEC/Volume7Issue5/IJEC-2019-05-16-2.pdf>.
2. Manisha Nilkanth, (2019). [American Sign Language Translator and Calling Device](https://ipasj.org/IJEC/Volume7Issue5/IJEC-2019-05-14-1.pdf). *IPASJ International Journal of Electronics & Communication (IJEC)*. 7(5), 1-5. ISBN/ ISSN No : ISSN 2321-5984. URL : <https://ipasj.org/IJEC/Volume7Issue5/IJEC-2019-05-14-1.pdf>.
3. Ms. Manisha Nilkanth, (2013). [Handwritten Devnagri Numerals recognition using PCA & MSE](http://www.prjpublication.com/Paperlist.asp). *International journal on Electronics & Communication Engineering Research & Development (IJECERD)*. Volume 3(1), 39-47. ISBN/ ISSN No : ISSN-2248-9525(print) ISSN-2248-9533 (online) . URL : <http://www.prjpublication.com/Paperlist.asp>.

## Conference Publications

1. Manisha Nilkanth, (2016). [Fabrication of Do It Yourself Solar Kit: Democratizing solar energy applications](#). *2016 Biennial International Conference on Power and Energy Systems: Towards Sustainable Energy (PESTSE)*. 1(7516363), 1-6. ISBN/ ISSN No : 978-1-4673-6660-1 .
2. Manisha Nilkanth, (2010). Devnagri Numeral Recognition . *International Journal of Emerging Technologies and Applications in Engineering, Technology & Science*. 1(1), 210-216. ISBN/ ISSN No : 0974-3588.

## Patent

1. Raviraj Gurav, (2016). 201621028759 . [A FULLY AUTOMATIC WATER LEVEL CONTROLLER WITH MAGNETIC SENSOR](#). 02/09/2016, India.
2. Raviraj Gurav, Sanjay Gaikwad, Preeti Purohit, (2016). 20621012035. [A modified silencer with unique protective shield](#). 12/02/2016, India.
3. SANJAY GAIKWAD, PRITEE PUROHIT, RAVIRAJ GURAV, MANISHA NILKANTH, (2016). 20621012053A. [Work bench with USB microscope for Art on Rice](#). 12/02/2016, India.
4. manisha nilkanth, (2016). 4821/MUM/2015. [Economical multipurpose cutter for Shankarpali, puri, disco papad and other such items](#). 15/01/2016, India.
5. Raviraj Gurav, (2016). 4822/MUM/2015. [Braking system with better vehicle stability using non uniform contact pressure for two wheeler](#). 15/01/2016, India.

## Guest Lecture

1. MATLAB & it's applications in signal processing (Organized), signal processing, 2016, Army Institute of Technology, Pune

## Workshop

1. Testing for Manisha Mam (Attended), Testing for Manisha Mam, 2019, Pune
2. faculty awareness program (Organized), faculty awareness program , 2018, Army Institute of technology, Pune
3. IoT and Python Programming (Organized), IoT and Python Programming, 2018, Army Institute of Technology
4. CO, PO Mapping (Attended), CO, PO Mapping for NBA, 2018, Army Institute of technology, Pune
5. Foundation program in ICT for education (Attended), moodle, word press etc, 2018, Army Institute of Technology, Pune
6. Embeded system and Microcontroller applications (Attended), various Microcontrollers introduction, 2017, COEP Pune
7. PCB Designing (Organized), Proteus simulation and PCB designing, 2017, Army Institute of Technology, Pune
8. PCB Designing (Organized), Proteus simulation and PCB designing, 2016, Army Institute of Technology, Pune

## Research Grants

Title Of Project	Funding Agency	Duration	Sanctioned Amount	Role of Investigator
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### Consultancy

Title Of Project	Funding Agency	Duration	Sanctioned Amount	Investigator Role
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### Professional Memberships

Society	Membership No	Duration	
ISTE	LM71939	01/06/2010	31/12/2050

No of PhD Students Guided : 0

No of PG Students Guided : 0