

## STAFF PROFILE



**Mr. Avinash Subhash Patil**

Assistant Professor

Electronics & TeleCommunication

apatil@aitpune.edu.in

*Date of Joining*  
05/07/2005

*Nature of Appointment*  
Regular

### Educational Details

| Qualification | Degree                            | University              | Grade | Year |
|---------------|-----------------------------------|-------------------------|-------|------|
| SSC           |                                   | Maharashtra state board | 64.40 | 1996 |
| Diploma       | Electronics and Telecommunication | MSBTE                   | 59    | 1999 |
| Diploma       | Industrial Electronics            | MSBTE                   | 67.52 | 2000 |
| Graduation    | Electronics                       | Shivaji University      | 67    | 2003 |
| PG            | Electronics-VLSI                  | Bharati Vidyapeeth      | 67    | 2007 |

### Areas of Interest

### Experience Details

| Type        | Duration/ Period              | Designation/ Organisation Name                      | Brief Job Profile   |
|-------------|-------------------------------|---|---|
| Industrial  | 1.00 Years<br>June 2003-04    | Software Engg<br>Automize Controls                  | VHDL Coder  |
| Educational | 1.00 Years<br>June 2004-05    | Lecturer<br>Bharati Vidyapeeth University<br>COE    | Teaching subjects like<br>Microprocessor Based system and<br>Elements of Information technology |
| Educational | 4.00 Years<br>July 2005- 2009 | Lecturer<br>Army Institute of Technology            | Lecturer  |
| Educational | 1.00 Years<br>July 2009-10    | Sr. Lecturer<br>Army Institute of Technology        | Sr Lecturer   |
| Educational | 7.00 Years<br>July 2010       | Assistant Professor<br>Army Institute of Technology | Assistant Professor in 6th Pay  |

### Workshop

1. IOT certification Workshop by TSSC (Attended), IOT certification Workshop by TSSC, 2018, AIT, Pune
2. Workshop on TE E&TC Revised Syllabus ESMP (Attended), Employability skills in Electronics design, 2017, IsquareIT, Pune
3. National Level Two Weeks Short Term Training Program on Ethics and moral values (Attended), Ethics and moral values, 2017, SKNCOE, Pune
4. Participated in 4 month training and task based program conducted by IIT Powai. (Attended), Task based training, 2017, AIT, Pune as remote center
5. Seminar for Maple soft product overview (Attended), Maple soft product overview, 2017, AIT, Pune
6. Attend two days workshop at IIT Powai\_eyantra (Attended), e\_yantra, 2017, IIT, Powai

### Research Grants

| Title Of Project                                    | Funding Agency | Duration   |            | Sanctioned Amount | Role of Investigator   |
|---|----------------|------------|------------|-------------------|------------------------|
| Designing of a CMOS based Analog Multiplier circuit | BCUD           | 01/06/2011 | 01/06/2013 | 200000.00         | Principal Investigator |

### Consultancy

| Title Of Project | Funding Agency | Duration | Sanctioned Amount | Investigator Role |
|------------------|----------------|----------|-------------------|-------------------|
|------------------|----------------|----------|-------------------|-------------------|

### Professional Memberships

| Society | Membership No | Duration   |            |
|---------|---------------|------------|------------|
| ISTE    | LM50616       | 01/06/2006 | 01/06/2041 |

### Achievements/ Contribution

| Year | Details |
|------|---------|
|------|---------|

**No of PhD Students Guided :** 0      **No of PG Students Guided :** 0  
**No of PhD Awarded Students :** 0